THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Bronner et al.

Art Unit: 2814

Serial No.: 09/105,739

Dkt, No.: BU9-97-149

Filed: 6/26/98

Examiner: Rao, Shrinivas H.

11 C Aust N/E M. Brusson 5/21/03

Title: SELF-ALIGNED SURFACE-TRAP MINT DRAM CELL

Commissioner for Patents Washington, DC 20231

AMENDMENT

Sir:

In response to the Final Office Action mailed March 17, 2003, please amend the aboveidentified application as follows:

In the Claims:

Currently pending claims 1-29 for consideration by the Examiner are as follows:

- 1. (Previously Amended) A method for forming an interconnection between a storage capacitor and a transfer device in a memory cell, the method comprising the steps of:
 - a) forming a capacitor having a lip extending over a top surface of a substrate; and
 - b) diffusing dopant from the lip into the top surface of the substrate, the diffusing dopant forming a surface strap in the substrate, the surface strap providing a connection between

the capacitor and the transfer device.

CERTIFICATE OF TRANSMISSION BY FACSIMILE (37 CFR 1.8) Applicaph(s): Brouner et al.				Docket No. BU9-97-149	
- //	al No. 05,739	Filing Date 6/26/98	Examiner Rao, Shrinivas H.	Group Art Unit 2814	
Invention:	_SEL#-ALIGNED S	URFACE-TRAP MINT D	RAM CELI,		
I hereby certify that thisis being facsimile transmitted to the Ur		o the United States Pater	Amendment (18 pages) (Identify type of correspondence) ed States Patent and Trademark Office (Fax. No. 703-308-7722		
on	5/19/2003 (Date)	<u></u> .			
			Kim Dwileski (Typed or Urimed Name of Person Signing Cartificate) (Signature)		

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